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U.S. UTILITY Patent Application

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**CONTINUING DATA VERIFIED:

NONE

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-140622 05/11/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO XA-9635
TITLE : Manufacturing method of semiconductor device			
U.S. DEPT. OF COMM./PAT. & TM-PTO-438L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Primary Examiner	
Amount Due	Date Paid	Application Examiner	
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